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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/697,610	10/31/2003	Bily Wang	Harvatek 9240	9400	
7590 09/07/2004			EXAM	EXAMINER	
HungChang LIN			HO, TU TU V		
8 Schindler Ct. Silver Spring, MD 20903			ART UNIT	PAPER NUMBER	
1 0			2818		
		DATE MAILED: 09/07/2004			

Please find below and/or attached an Office communication concerning this application or proceeding.

				200			
		Application No.	Applicant(s)				
Office Action Summary		10/697,610	WANG ET AL.				
		Examiner	Art Unit				
		Tu-Tu Ho	2818				
Period fo	The MAILING DATE of this communication app or Reply	pears on the cover sheet v	vith the correspondence addres	is			
THE I - Exter after - If the - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period vere to reply within the set or extended period for reply will, by statute eply received by the Office later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a y within the statutory minimum of th will apply and will expire SIX (6) MO , cause the application to become A	reply be timely filed irty (30) days will be considered timely. NTHS from the mailing date of this communication (35 U.S.C. § 133).	nication.			
Status							
1)🛛	Responsive to communication(s) filed on 31 O	october 2003.					
2a) <u></u> ☐	☐ This action is FINAL . 2b)☐ This action is non-final.						
3)🖾	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispositi	on of Claims						
5)□ 6)□ 7)□ 8)□ Applicati 9)□ 10)⊠	Claim(s) 1-8 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw Claim(s) is/are allowed. Claim(s) is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or on Papers The specification is objected to by the Examine The drawing(s) filed on 31 October 2003 is/are Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct	or election requirement. er. : a)⊠ accepted or b)□ drawing(s) be held in abeya tion is required if the drawin	ance. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFR 1.				
11)	The oath or declaration is objected to by the Ex	kaminer. Note the attache	ed Office Action or form PTO-1	52.			
Priority ι	under 35 U.S.C. § 119						
a)l	Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Bureau See the attached detailed Office action for a list	ts have been received. Is have been received in rity documents have bee u (PCT Rule 17.2(a)).	Application No n received in this National Stag	ge			
Attachmen		∆ □	. S (DTO 442)				
2) 🔲 Notic 3) 🔲 Infori	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	Paper No	/ Summary (PTO-413) o(s)/Mail Date · Informal Patent Application (PTO-152 	2)			

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DETAILED ACTION

1. This application is in condition for allowance except for the following formal matters:

a. Typographical error in Claim 1, line 6:

"serving as said one terminal and said second terminal respectively", which should be:

"said far ends serving as terminals for the IC package"

b. Typographical error in Claim 7:

"coupling is by flip-chip", which should be:

"coupling is by flip-chip technology"

Allowable Subject Matter

The prior art of record fails to teach or render obvious an IC package with all limitations as recited in claim 1, comprising a first section and a second section of a common stacked metal substrate with an upper layer and a lower layer, an IC chip having two electrodes each coupled to each of the two sections respectively, a glue to hold the two sections together so as to leave the far ends of the two sections uncovered, the far ends serving as terminals for the IC package, characterized in that the common stacked metal substrate is metal.

As allowable subject matter has been indicated, applicant's reply must either comply with all formal requirements or specifically traverse each requirement not complied with. See 37 CFR 1.111(b) and MPEP § 707.07(a).

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Conclusion

3. Prosecution on the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO MONTHS** from the mailing date of this letter.

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Copies of the references are not being furnished with this Office Action per MPEP § 707.05(a).

U.S. Patent 6,770,498 to Hsu discloses a process for fabricating light emitting diode (LED) packages from a plate-like frame having a plurality of cells.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tu-Tu Ho whose telephone number is (571) 272-1778. The examiner can normally be reached on 6:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DAVID NELMS can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

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system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tu-Tu Ho

August 26, 2004

David Neims

Supervisory Patent Examiner Technology Center 2800